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application of:

G. Glenn Henry; Thomas C. McDonald

erial No.:

09/849799

Filed:

05/04/01

Docket:

CNTR.2052

For:

SPECULATIVE BRANCH TARGET ADDRESS CACHE WITH SELECTIVE OVERRIDE BY SECONDARY PREDICTOR BASED ON BRANCH INSTRUCTION

#### INFORMATION DISCLOSURE STATEMENT SUBMITTED UNDER 37 CFR 1.97(D) AND 1.97 (E)(2)

Attached hereto is Form PTO-1449 listing documents believed relevant to the subject application. It is respectfully requested that the Examiner review the information disclosed herein in detail, independently evaluate each item carefully in the consideration of the pending claims and return an initialed copy of each form to the undersigned.

This disclosure statement should not be construed as a representation that a search has been made, that no other material information as defined in 37 C.F.R. § 1.56(a) exists, or as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 CFR § 1.56(b) or is available as a reference under 35 U.S.C. § 102 et seq. Applicant reserves the right to swear behind or otherwise disprove the alleged "prior" nature of any art cited should the facts support and the situation warrant

It is believed that this disclosure complies with the requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98, and the Manual of Patent Examining Procedures § 609. If for some reason the examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.

A copy of each document is enclosed. Some of the documents may have markings thereon. No significance is intended to be attached to the markings.

No item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in § 1.56(c) more than three months prior to the filing of the information disclosure statement. The information cited in the information disclosure statement was cited by the United States Patent Office in one or more applications that are related to the present application.

Also attached hereto is a check in the amount of \$180.00 for the fee set forth in § 1.17(p).

Respectfully submitte

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Date: 13-10-04

"EXPRESS MAIL" mailing label number <u>EDDD340073600</u> Date of Deposit <u>12-75-04</u>. I hereby certify that this paper is being deposited with the U.S. Postal Service Express Mail Post Office to Addressee Service under 37 C.F.R. §1.10 on the date shown above and is addressed to the U.S. Commissioner of

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Application Number	09/849799	
Filing Date	05/04/01	
First Named Inventor	G. Glenn Henry	
	Thomas C. McDonald	
Group Art Unit	2183	
Examiner Name	Tonia Moenske	
Attorney Docket Number	CNTR.2052	

Examiner Initials	Cite	U.S. Patent Do		Name of Patentee or Applicant of Cited Document	Date of Publication	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	No.1	Number	Kind Code <sup>2</sup>		of Cited Document MM-DD-YYYY	
	AA	5142634		Fite et al	08/25/1992	
	AB	5163140		Stiles et al	11/10/1992	
	AC	5353421		Emma et al	10/04/1994	
	AD	5355459		Matsuo et al	10/11/1994	· · · · · · · · · · · · · · · · · · ·
	AE	5404467		Saba et al	04/04/1995	
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	AH	5761723		Black et al	06/02/1998	
	Al	5812839		Hoyt et al	09/22/1998	
	AJ	5850543		Shiell et al	12/15/1998	
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	AM	5964868		Gochman et al	10/12/1999	***************************************
	AN	5974543		Hilgendorf et al	10/26/1999	
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	AU	6502185		Keller et al	12/31/2002	
	AV	6647467		Eric M. Dowling	11/11/2003	

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Group Art Unit	2183	
Examiner Name	Tonia Moenske	
Attorney Docket Number	CNTR.2052	

	OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS		
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
	ВА	SAKAMOTO ET AL; Microarchitecture Support for Reducing Branch Penalty in a Superscaler Processor; pp. 208-216; IEEE 1996; Mitsubishi Electric Corp., System LSI Laboratory, 4-1 Mizuhara, Itami, Hyogo 664, Japan.	
	BB	YEH ET AL; <u>ALTERNATIVE IMPLEMENTATION OF TWO-LEVEL ADAPTIVE BRANCH PREDICTION;</u> 19 <sup>TH</sup> Annual International Symposium on Computer Architecture, pp. 124-134, May 19-21, 1992, Gold Coast, Australia.	
	ВС	CHANG ET AL; ALTERNATIVE IMPLEMENTATIONS OF HYBRID BRANCH PREDICTORS; Proceedings of MICRO-28, 1995, IEEE.	
	BD	MC FARLING, SCOTT; <u>WRL TECHNICAL NOTE TN-36</u> , "Combining Branch Predictors, June 1993, Western Research Laboratory, 250 University Ave., Palo Alto, CA 94301	
	BE	IEEE 100; THE AUTHORITATIVE DICTIONARY OF IEEE STANDARDS TERMS; Seventh Edition, IEEE, Standards Information Network, IEEE Press,	
	BF	BRAY ET AL; STRATEGIES FOR BRANCH TARGET BUFFERS; Technical Report No. CSL-TR-91-480, June 1991.	
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